



Attorney Docket No.: CYPR-CD00169.CON

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Patent Application

Inventor(s): Snyder et al.

Group Art Unit: 2819

Filed: 03/16/04

Examiner: Not yet assigned

Application No.: 10/803,030

Title: PROGRAMMABLE MICROCONTROLLER ARCHITECTURE (MIXED ANALOG/DIGITAL)

Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
ATI	A	6,144,327	11/2000	Distinti et al.			
	B	6,018,559	01/2000	Azegami et al.			
	C	5,202,687	04/13/93	Distinti	341	158	06/12/91
	D	5,880,598	03/09/99	Duong	326	41	01/10/97
	E	6,304,101	10/16/01	Nishihara	326	41	06/02/00
	F	5,426,378	06/20/95	Ong	362	39	04/20/94
	G	5,828,693	10/27/98	Mays et al.	375	202	03/21/96
	H	5,703,871	12/30/97	Pope et al.	370	248	05/26/95
	I	6,018,559	01/25/00	Azegami et al.	377	79	12/16/96

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
ATI	J	CYPR-CD00170; "DIGITAL CONFIGURABLE MACRO ARCHITECTURE"; 07/18/01; 09/909,045; W. Snyder 09/909,045
	K	CYPR-CD00172; "CONFIGURING DIGITAL FUNCTIONS IN A DIGITAL CONFIGURABLE MACRO ARCHITECTURE"; 07/18/01; 09/909,109; W. Snyder
	L	CYPR-CD00173; "A PROGRAMMABLE ANALOG SYSTEM ARCHITECTURE (AS AMENDED)"; 07/18/01; 09/909,047; M. Mar
	M	CYPR-CD00232; "PROGRAMMABLE SYSTEM ON A CHIP"; 10/01/01; 10/033,027; W. Snyder
Examiner		Date Considered
[Signature]		12/10/06

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.